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	Substitute for form 1449A/PTO			Application Number	10/720,466	
	INFORMATION DE	SCI OSLIE) E	Filing Date	11/24/03	
	INFORMATION DISCLOSURE STATEMENT BY APPLICANT			First Named Inventor	Correale, Jr. et al.	
				Art Unit	2819	
	<u> </u>			Examiner Name	D. Chans	
She	et 1	of	2	Attorney Docket Number	YOR920030373US1	

			U.S. PATENT	DOCUMENTS	
Examiner Initials*	Cite No.	Document Number Number - Kind Code ² (# known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
OC.		US- 5594368		USAMI	
190	L	US- 5818256		USAMI	
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	FOREIGN PATENT DOCUMENTS							
	Cite No.1	Foreign Patent Document	Cublication Cata	Name of Patentee or	Pages, Columns, Lines,	۳⁰		
Examiner Initials*		Country Code ³ - Number ⁴ - Kind Code ⁵ (if known)	Publication Date MM-DD-YYYY	Applicant of Cited Document	Where Relevant Passages or Relevant Figures Appear			
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Translation is attached.

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Substitute for form 1449B/PTO			Complete if Known			
			Application Number	4720 466		
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			First Named Inventor	Correate, Jr. et al.		
(use as many sheets as necessary)			Art Unit	2819		
			Examiner Name	D- Chang.		
2	of	2	Attorney Docket Number	YOR920030359US1 /		
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		NON PATENT LITERATURE DOCUMENTS	
Examiner Initials*	Cite, No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
2c		LACKEY ET AL., Managing power and performance for System-on-Chip designs using Voltage Islands, Proceedings of the 2002 IEEE/ACM international conference on Computer-aided design , 11/11/2002, Page(s) 195-202	
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DC		YEH ET AL., LAYOUT TECHNIQUES SUPPORTING THE USE OF DUAL SUPPLY VOLTAGES FOR CELL-BASED DESIGNS, Proc. ACM/IEEE Design Auto. Conf., 6/1/1999, Page(s) 62-67	

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